

Re: conv_integer

Source: <http://newsgroups.derkeiler.com/Archive/Comp/comp.arch.fpga/2006-02/msg01569.html>

- *From:* "John Adair" <removethisthenleavejea@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx>
 - *Date:* Tue, 28 Feb 2006 15:58:09 -0000
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Marco

This is only a VHDL function defined in a library. Effectively it in itself is instant. Your speed, frequency, or number of clocks will be determined by the functions around it and how data is used.

John Adair

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"Marco T." <marc@xxxxxxxxxx> wrote in message
[news:du1or7\\$6fp\\$1@xxxxxxxxxxxxxxxx](mailto:news:du1or7$6fp$1@xxxxxxxxxxxxxxxx)

Hallo,
how much clock cycles takes a conv_integer operation?

Many Thanks
Marco